



Receipt  
IFW

PATENT  
Docket No.: APPL-P2832COA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2133

Examiner: Not yet assigned

Serial No.: 10/802,576

Filed: March 16, 2004

In re Application of: Colin Whitby-Strevens

For: METHOD AND APPARATUS FOR GENERATING JITTER TEST PATTERNS ON  
A HIGH PERFORMANCE SERIAL BUS

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Office of Initial Patent Examination's Customer Service Center, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 7/14/04, Signed Krista Thompson  
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**REQUEST TO CORRECT FILING RECEIPT**

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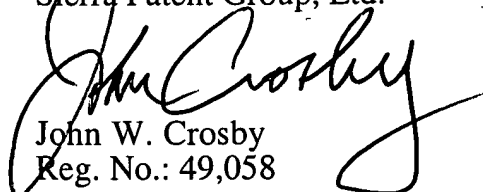
It is respectfully submitted that the Filing Receipt in the above-identified application is incorrect for the following reason(s):

"The docket number APPL-2832COA" should read -APPL-P2832COA--.

Please see the marked up version of the filing receipt.

Please issue a corrected filing receipt.

Respectfully submitted,  
Sierra Patent Group, Ltd.

  
John W. Crosby  
Reg. No.: 49,058

Dated: July 14, 2004

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10/802,576	03/16/2004	2133	770	APPL-2832COA P2832COA	5	1	1

CONFIRMATION NO. 3482

28661  
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## FILING RECEIPT



\*OC000000012837690\*

CPL

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## Applicant(s)

Colin Whitby-Stevens, Bristol, UNITED KINGDOM;

## Assignment For Published Patent Application

Apple Computer, Inc.;

## Domestic Priority data as claimed by applicant

This application is a CON of 09/557,071 04/21/2000 PAT 6,718,497

## Foreign Applications

If Required, Foreign Filing License Granted: 06/01/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

## Title

Method and apparatus for generating jitter test patterns on a high performance serial bus

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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